250 MS/s, 125 MHz, 8-Bit Digitizers

NI 5114 NEW!

- 250 MS/s real-time sampling
- 5 GS/s random-interleaved sampling
- 8-bit resolution
- 125 MHz bandwidth
- 40 mV_{pp} to 40 V_{pp} input range
- 8, 64, or 256 MB memory per channel
- Edge, window, hysteresis, video, and digital triggering with 40 ps timestamping

Calibration
- Gain, offset, compensated 1 MΩ attenuator, and timing self-calibration
- 2-year external calibration interval

Overview

Applications
- Aerospace/Defense
- Radar, Sonar, and DODAR
- Satellite
- Signal intelligence

Biomedical and Scientific Research
- Ultrasound imaging
- Mass spectrometry
- Particle physics

Communications
- Wi-Fi
- Wireless communications
- Baseline I & Q

National Instruments PXI-5114 and PCI-5114 high-speed digitizers feature two 250 MS/s simultaneously sampled input channels with 8-bit resolution, 125 MHz bandwidth, and up to 256 MB of memory per channel in a compact, 3U PXI or PCI device. With the National Instruments Synchronization and Memory Core (SMC) architecture of an NI 5114, you can create mixed-signal systems using signal generators and digital waveform generator/analyzers or build a high-channel-count digitizer with subnanosecond synchronization between channels. An NI 5114 is ideal for a wide range of application areas including communications, scientific applications, military/aerospace, and consumer electronics.

Dual 250 MS/s, 8-Bit Input Channels

- 125 MHz input bandwidth with noise filters
- 5 GS/s equivalent-time sampling (ETS) for repetitive signals
- Independent channel-selectable 40 mV_{pp} to 40 V_{pp} input ranges
- Independent channel-selectable 50 Ω or 1 MΩ input impedance
- 2-year calibration interval and 0 to 55 °C operating temperature

Deep Onboard Memory

- 8, 64, or 256 MB of memory per channel
- Capture more than 1 million triggered waveforms in multiple record mode with hardware trigger rearming
- Stream data continuously from onboard memory to host memory or disk

Operating Systems

- Windows Vista/XP/2000
- LabVIEW Real-Time

Recommended Software

- LabVIEW
- LabWindows™/CVI
- Measurement Studio for Visual Studio
- LabVIEW SignalExpress

Driver Software (included)

- NI-SCOPE Soft Front Panel and driver
- LabVIEW Express VIs

Triggering, Clocking, and Synchronization

- Edge, window, hysteresis, and digital triggering with 40 ps timestamping
- Pretrigger and posttrigger acquisition in single- and multiple-record mode
- Internal 250 MHz clock or external clock from 50 to 250 MHz
- Phase lock to PXI 10 MHz reference or external reference from 1 to 20 MHz
- Timestamp-triggered events with 100 ps resolution

Software

- IVI-compliant NI-SCOPE driver for NI LabVIEW and LabWindows/CVI as well as Microsoft C++ and Visual Basic with more than 50 built-in measurements
- NI-SCOPE Soft Front Panel for interactive control

Calibration

- Gain, offset, compensated 1 MΩ attenuator, and timing self-calibration
- 2-year external calibration interval

Ordering Information

NI PXI-5114 .......................................................... 779466-0M
NI PCI-5114 .......................................................... 779745-0M

1M (memory per channel): 1 (8 MB), 2 (64 MB), 3 (256 MB)
Includes NI-SCOPE Soft Front Panel and driver.

Recommended PXI Switch
NI PXI-2593 (500 MHz mux/matrix) ........................................ 778793-01

BUY NOW!

For complete specifications, pricing, and accessory information, call 800 813 3693 (U.S.) or go to ni.com/digitizers.
Specifications

These specifications are valid for 0 to 55 °C, unless otherwise stated.

Acquisition System

Number of channels............................ 2 simultaneously sampled
Vertical resolution............................... 8 bits
Bandwidth (-3 dB) ............................... 80 MHz

<table>
<thead>
<tr>
<th>Range (Vpp)</th>
<th>Minimum Bandwidth</th>
</tr>
</thead>
<tbody>
<tr>
<td>All except 0.04</td>
<td>125 MHz</td>
</tr>
<tr>
<td>0.04</td>
<td>100 MHz</td>
</tr>
</tbody>
</table>

Bandwidth limit filters
(software-selectable) ................................. 20 MHz noise filter
Maximum sampling rate............................. 250 MS/s real-time sampling,
5 GS/s equivalent-time/random-interleaved sampling
Onboard sample memory............................ 8, 64, or 256 MB per channel
(8, 64, or 256 million samples)

Multiple Record Acquisition (0 to 100% pretrigger and postrigger data)

<table>
<thead>
<tr>
<th>Memory/Channel</th>
<th>Maximum Number of Records</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 MB</td>
<td>32,768</td>
</tr>
<tr>
<td>64 MB</td>
<td>100,000</td>
</tr>
<tr>
<td>256 MB</td>
<td>100,000</td>
</tr>
</tbody>
</table>

1Infinite in streaming configuration.

Input impedance ..................................... 50 Ω and 1 MΩ || 26 pF,
software-selectable

Full-Scale Input Range and Programmable Vertical Offset

<table>
<thead>
<tr>
<th>Range (Vpp)</th>
<th>Vertical Offset Range (V)</th>
<th>Range (Vpp)</th>
<th>Vertical Offset Range (V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.04</td>
<td>±0.8</td>
<td>0.04</td>
<td>±0.8</td>
</tr>
<tr>
<td>0.1</td>
<td>±0.8</td>
<td>0.1</td>
<td>±0.8</td>
</tr>
<tr>
<td>0.2</td>
<td>±0.8</td>
<td>0.2</td>
<td>±0.8</td>
</tr>
<tr>
<td>0.4</td>
<td>±0.8</td>
<td>0.4</td>
<td>±0.8</td>
</tr>
<tr>
<td>1.0</td>
<td>±0.5</td>
<td>1.0</td>
<td>±0.8</td>
</tr>
<tr>
<td>2.0</td>
<td>±0.0</td>
<td>2.0</td>
<td>±0.8</td>
</tr>
<tr>
<td>4.0</td>
<td>±0.0</td>
<td>4.0</td>
<td>±0.8</td>
</tr>
<tr>
<td>10</td>
<td>±2.0</td>
<td>10</td>
<td>±30</td>
</tr>
<tr>
<td>-</td>
<td>-</td>
<td>20</td>
<td>±25</td>
</tr>
<tr>
<td>-</td>
<td>-</td>
<td>40</td>
<td>±15</td>
</tr>
</tbody>
</table>

Maximum input overload.......................... 50 Ω: 7 Vrms with 1 peaks I ≤10 V; 1 MΩ: 1 peaks I ≤35 V
Input coupling ..................................... AC, DC, GND – AC coupling available on 1 MΩ only
AC coupling cutoff frequency (-3 dB)

Accuracy

DC accuracy (0 V offset setting) ............... ±(1.5% of input +0.3% of FS + 200 μV) for PXI-5114
Channel-to-channel crosstalk .................... -60 dB at 10 MHz

Spectral Characteristics (typical)

Dynamic performance (10 MHz, -1 dBFS input signal)

<table>
<thead>
<tr>
<th>Range (Vpp)</th>
<th>All Ranges Except 0.04</th>
<th>0.04</th>
</tr>
</thead>
<tbody>
<tr>
<td>SFDR</td>
<td>58 dBc</td>
<td>58 dBc</td>
</tr>
<tr>
<td>THD</td>
<td>-58 dBc</td>
<td>-58 dBc</td>
</tr>
<tr>
<td>ENOB</td>
<td>7.2</td>
<td>6.2</td>
</tr>
<tr>
<td>SINAD</td>
<td>44 dB</td>
<td>38 dB</td>
</tr>
</tbody>
</table>

RMS Noise

20 MHz noise filter enabled .......................... 0.28% of FS

Timebase System

Timebase options .................................. Internal, external (CLK IN)
Internal
Internal sample clock frequency ............... 250 MS/s sampling rate with decimation by n, 1 ≤ n ≤ 65,535
Timebase accuracy ............................... ±25 ppm (±0.0025%)

External
External clock sources ........................... CLK IN (SMB connector)
External clock range .................................. 50 to 250 MHz with decimation by n where 1 ≤ n ≤ 65,535
External reference sources ....................... CLK IN (SMB connector), PXI_CLK10 (PXI backplane 10 MHz)
External reference range ........................ 1 to 20 MHz in 1 MHz increments
External clock/reference amplitude ............. Sine wave: 0.65 to 2.8 Vpp (0 to 13 dBm)
Square wave: 0.2 to 2.8 Vpp
External clock/reference impedance .......... 50 Ω, AC coupled

Trigger System

Modes .............................................. Edge, hysteresis, window, video, digital, immediate, software
Sources ............................................ CH 0, CH 1, TRIG, PXI_Trig, <0..6>, PXI star, software
Slope .............................................. Rising or falling
Hysteresis ....................................... Fully programmable
High-frequency reject filter ..................... 50 kHz software-selectable
Low-frequency reject filter ...................... 50 kHz software-selectable
Sensitivity

CH 0 and CH 1 .................................. 5% FS
TRIG .............................................. 0.5 Vpp
Level accuracy

CH 0, CH 1 ...................................... ±5% FS up to 10 MHz
TRIG .............................................. ±0.5 V up to 10 MHz
Time resolution .................................. 40 ps with time-to-digital converter enabled
Rearm time .............................. 2 µs
Holdoff ................................. From 2 µs to [(2^35)-1] x (sample clock period), software-selectable

1Time-to-digital converter disabled.
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External Trigger Channel (TRIG)

- Impedance: 1 MΩ || 22 pF
- Vertical range: ±5 V
- Coupling: AC, DC

Intermodule SMC Synchronization Using NI-TClk (typical)

- Skew: 500 ps ≤20 ps after manual adjustment

Power Requirements (typical)

<table>
<thead>
<tr>
<th></th>
<th>+3.3 VDC</th>
<th>+5 VDC</th>
<th>+12 VDC</th>
<th>-12 VDC</th>
<th>Total Power</th>
</tr>
</thead>
<tbody>
<tr>
<td>PXI</td>
<td>840 mA</td>
<td>1.1 A</td>
<td>250 mA</td>
<td>170 mA</td>
<td>13.32 W</td>
</tr>
<tr>
<td>PCI</td>
<td>1.6 A</td>
<td>1.7 A</td>
<td>45 mA</td>
<td>0 A</td>
<td>14.32 W</td>
</tr>
</tbody>
</table>

Environment

- Operating temperature: 0 to 55 °C for PXI-5114, 0 to 45 °C for PCI-5114 (meets IEC-60068-2-1 and IEC-60068-2-2)
- Storage temperature: -40 to 71 °C (meets IEC-60068-2-1 and IEC-60068-2-2) 10 to 90%, noncondensing (meets IEC-60068-2-56)

Calibration

- Self-calibration: Gain, offset, compensated 1 MΩ attenuator, triggering, and timing for all input ranges
- External calibration interval: 2 years

Safety and Compliance

Safety

This product is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1

Note: For UL and other safety certifications, refer to the product label or visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Electromagnetic Compatibility

This product is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326 EMC requirements; Minimum Immunity
- EN 55011 Emissions; Group 1, Class A
- CE, C-Tick, ICES, and FCC Part 15 Emissions; Class A

Note: For EMC compliance, operate this device according to product documentation.

CE Compliance

This product meets the essential requirements of applicable European Directives, as amended for CE marking, as follows:

- 2006/95/EC; Low-Voltage Directive (safety)
- 2004/108/EC; Electromagnetic Compatibility Directive (EMC)

Note: Refer to the Declaration of Conformity (DoC) for this product for any additional regulatory compliance information. To obtain the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Waste Electrical and Electronic Equipment (WEEE)

EU Customers: At the end of their life cycle, all products must be sent to a WEEE recycling center. For more information about WEEE recycling centers and National Instruments WEEE initiatives, visit ni.com/environment/weee.htm.

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NI Factory Installation Services (FIS) is the fastest and easiest way to use your PXI or PXI/SCXI combination systems right out of the box. Trained NI technicians install the software and hardware and configure the system to your specifications. NI extends the standard warranty by one year on hardware components (controllers, chassis, modules) purchased with FIS. To use FIS, simply configure your system online with ni.com/pxiadvisor.

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